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APPLICATION NO. FILING DATE		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/986,727	6,727 11/09/2001		Chien- Ming Chen	0941-0364P-SP	5871	
2292	7590	02/22/2005		EXAMINER		
BIRCH ST PO BOX 74		KOLASCH & BI	DU, THUAN N			
		A 22040-0747	ART UNIT	PAPER NUMBER		
				2116		
				DATE MAILED: 02/22/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	on No.	Applicant(s)				
			27	CHEN ET AL.				
	Office Action Summary	Examine	r	Art Unit				
		Thuan N.	Du	2116				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
THE - External after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNI Insions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this common period for reply specified above is less than thirty (30) period for reply is specified above, the maximum stare to reply within the set or extended period for reply reply received by the Office later than three months a ed patent term adjustment. See 37 CFR 1.704(b).	CATION. of 37 CFR 1.136(a). In no ex unication.)) days, a reply within the sta tutory period will apply and w will, by statute, cause the app	vent, however, may a reply be time tutory minimum of thirty (30) days vill expire SIX (6) MONTHS from plication to become ABANDONEI	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status								
1)⊠	Responsive to communication(s) file	d on <u>02 Dec</u> ember 2	<u>2004</u> .	•				
2a)□	This action is FINAL .	2b)⊠ This action is r	non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims			•				
5)⊠ 6)⊠ 7)□	Claim(s) 1-7 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 4-7 is/are allowed. Claim(s) 1-3 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or election requirement.							
Applicati	ion Papers							
9)[The specification is objected to by the	Examiner.						
10)	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority u	ınder 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
Attachmen	t(s)							
	e of References Cited (PTO-892)	-	4) Interview Summary					
3) Inform	e of Draftsperson's Patent Drawing Review (P mation Disclosure Statement(s) (PTO-1449 or r No(s)/Mail Date		Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	atent Application (PTO-152)				

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DETAILED ACTION

1. Claims 1-7 are presented for examination.

2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claim Rejections - 35 USC § 102

- 3. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Strahlin (U.S. Patent No. 5,758,132).
- 4. Regarding claim 1, Strahlin teaches a method for reducing clock skew in an integrated circuit having a plurality of circuit blocks (16, 18), the method comprising:

providing a first source clock (clock circuitry 26) coupled to a clock input terminal first circuit block (16) within the circuit blocks [Fig. 1; col. 3, lines 61-64];

providing a second source clock (clock circuitry 28) coupled to a clock input terminal of second circuit block (18) within the circuit blocks [Fig. 1; col. 3, lines 61-64]; and

switching the clock input terminal of the second circuit block the first source clock when the second circuit block is configured to operate in synchronization with the first circuit block [col. 2, lines 30-44; col. 7, lines 42-67], thereby operating the first and second circuit blocks in accordance with the same first source clock [col. 4, lines 20-22].

5. Regarding claim 2, Strahlin further teaches:

providing an operation mode signal [col. 7, lines 15-18]; and

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setting a first state to the operation mode signal (PWOS = 1), thereby operating second circuit block synchronization with the first circuit block [col. 7, lines 15-18; col. 8, lines 48-49].

6. Regarding claim 3, Strahlin further teaches:

setting a second state to the operation mode signal, wherein the second circuit block operates asynchronously to the first circuit block when the operation mode signal is at the second state [col. 4, lines 40-41; col. 7, lines 15-18; col. 8, lines 37-39]; and

switching the clock input terminal the second circuit block to second source clock, thereby independently operating the first circuit block in accordance with the first source clock and the second circuit block in accordance with the second source clock [col. 4, lines 42-45].

Allowable Subject Matter

7. Claims 4-7 are allowed.

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The examiner can normally be reached on Monday and Wednesday-Friday: 9:30 AM - 8:00 PM, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on (571) 272-3670.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (703) 872-9306.

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9. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

Thuan N. Du

February 14, 2005